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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/865,145	05/24/2001	Louis Dominic Oliveira	010080	2679
23696	7590	11/01/2005	EXAMINER	
QUALCOMM, INC 5775 MOREHOUSE DR. SAN DIEGO, CA 92121			GARY, ERIKA A	
			ART UNIT	PAPER NUMBER
			2681	
DATE MAILED: 11/01/2005				

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 09/865,145	Applicant(s) OLIVEIRA, LOUIS DOMINIC	
	Examiner Erika A. Gary	Art Unit 2681	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 08 September 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-21 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-21 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 112

1. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

2. Claim 18 recites the limitation "the voice signals" in line 2. There is insufficient antecedent basis for this limitation in the claim.

Claim 18 recites the limitation "the music signals" in line 3. There is insufficient antecedent basis for this limitation in the claim.

Allowable Subject Matter

3. The indicated allowability of claims 1-11 is withdrawn in view of the newly discovered reference(s) to Yang (6,748,085). Rejections based on the newly cited reference(s) follow.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

5. Claims 1-21 are rejected under 35 U.S.C. 102(e) as being anticipated by Yang, US Patent Number 6,748,085 (hereinafter Yang).

Regarding claim 1, Yang discloses a device adapted to communicate with an audio mux, the audio mux receiving a vocoder input from a vocoder and a an audio decoder input from an audio decoder, the device comprising: a stereo/mono control unit coupled to a codec; the stereo/mono control unit receiving an audio mux input from the audio mux, the stereo/mono control unit providing a control output to the codec to reduce power consumption in the codec [col. 1: lines 59-63].

Regarding claim 2, Yang discloses the control output is coupled to a plurality of components in a receive audio processing path of the codec [col. 1: line 64 – col. 2: line 12].

Regarding claim 3, Yang discloses the plurality of components are in a right channel of the receive audio processing path [col. 3: lines 30-35].

Regarding claim 4, Yang discloses the plurality of components are in a left channel of the receive audio processing path [col. 3: lines 30-35].

Regarding claims 5 and 7, Yang discloses the control output disables at least one of the plurality of components to reduce power consumption in the receive audio processing path of the codec [col. 1: lines 59-63].

Regarding claims 6 and 9, it is inherent that the device includes a receive gain, receive filter, a digital to analog converter, a left/right selector, and a headset amp.

Regarding claim 8, Yang discloses the control output disables at least one of a plurality of components in a receive audio processing path of the codec when the audio

mux input received by the stereo/mono control unit comprises voice signals [col. 1: lines 59-63].

Regarding claim 10, Yang discloses the stereo/mono control unit further receives a plug-in detection input from a plug-in detection circuit [col. 3: lines 46-49].

Regarding claim 11, Yang discloses the plug-in detection circuit receives an I/O input from an I/O jack [col. 3: lines 46-49].

Regarding claim 12, Yang discloses a method of processing received audio signals in a device, the method comprising: disabling a first channel in a receive audio processing path and enabling a second channel in the receive audio processing path when the audio signals comprise mono signals; and enabling the first channel in the receive audio processing path and enabling the second channel in the receive audio processing path when the audio signals comprise stereo signals [col. 1: lines 59-63].

Regarding claim 13, Yang discloses the disabling of the first channel is performed by a stereo/mono control unit [col. 2: lines 1-12].

Regarding claim 14, Yang discloses the disabling of the first channel is performed by the control output of the stereo/mono control unit disabling at least one of a plurality of components in the first channel [col. 1: lines 59-63].

Regarding claim 15, it is inherent that the device includes a receive gain, receive filter, a digital to analog converter, a left/right selector, and a headset amp.

Regarding claim 16, Yang discloses the first channel is a right channel in the receive audio processing path and wherein the second channel is a left channel in the receive audio processing path [col. 3: lines 31-35].

Regarding claim 17, Yang discloses the first channel is a left channel in the receive audio processing path and wherein the second channel is a right channel in the receive audio processing path [col. 3: lines 31-35].

Regarding claim 18, it is inherent that the device comprises a vocoder and an audio decoder, wherein the vocoder provides voice signals to an audio mux, and wherein the audio decoder provides music signals to the audio mux.

Regarding claim 19, it is inherent that the stereo/mono control unit receives the audio signals from the audio mux.

Regarding claim 20, Yang discloses determining whether a stereo output component is coupled to the device [col. 2: lines 1-12].

Regarding claim 21, Yang discloses disabling the first channel when the stereo output component is not coupled to the device [col. 1 line 59 – col. 2: line 12].

6. Claims 12-15 are rejected under 35 U.S.C. 102(e) as being anticipated by Kurihara, US Patent Number 6,272,116 (hereinafter Kurihara).

Regarding claim 12, Kurihara discloses a method of processing received audio signals in a device, the method comprising: disabling a first channel in a receive audio processing path and enabling a second channel in the receive audio processing path when the audio signals comprise mono signals; and enabling the first channel in the receive audio processing path and enabling the second channel in the receive audio processing path when the audio signals comprise stereo signals [fig. 3; col. 4: lines 39-56].

Regarding claim 13, Kurihara discloses the disabling of the first channel is performed by a stereo/mono control unit [fig. 2: ref. 40].

Regarding claim 14, Kurihara discloses the disabling of the first channel is performed by the control output of the stereo/mono control unit disabling at least one of a plurality of components in the first channel [fig. 3; col. 4: lines 39-56].

Regarding claim 15, it is inherent that the device includes a receive gain, receive filter, a digital to analog converter, a left/right selector, and a headset amp.

Response to Arguments

7. Applicant's arguments with respect to claims 12-21 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

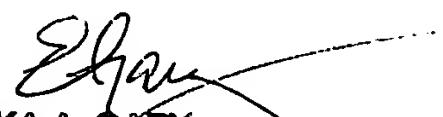
8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Adams, US Patent Number 6,594,366, discloses a headset/radio auto sensing jack.

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Erika A. Gary whose telephone number is 571-272-7841. The examiner can normally be reached on Monday-Thursday and alternate Fridays.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Joseph Feild can be reached on 571-272-4090. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information, about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

EAG
October 27, 2005


ERIKA A. GARY
PRIMARY EXAMINER